



# R.M.K. COLLEGE OF ENGINEERING AND TECHNOLOGY

(An Autonomous Institution)

Affiliated to Anna University, Chennai/Approved by AICTE, New Delhi

Accredited by NBA (All Eligible Courses)/NAAC with "A" GRADE An ISO 21001:2018 Certified Institution

R.S.M Nagar, Puduvoyal, Gummidipoondi Taluk, Thiruvallur District, Tamil Nadu-601206



## DEPARTMENT OF ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY)

Curates

# NEXUS CHIPCRAFT'26

Crafting Innovation in Silicon  
Not Just an Event – An Experience

on

9<sup>th</sup> February 2026

**FACULTY COORDINATOR**

Mr.R.SINGARAVELU., M.Tech(PhD)

**STUDENT COORDINATORS**

Mr.A.SAI SANJAY

Contact No.: 7339272233

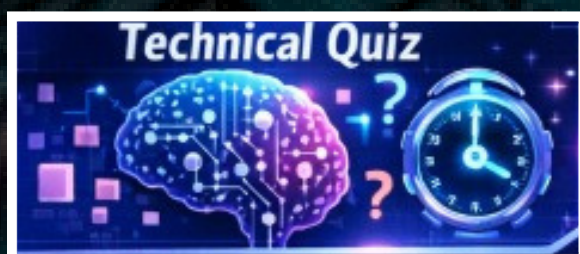
Ms.S.ANGELINE MALARVIZHI

₹ Cash prize will be given for winners

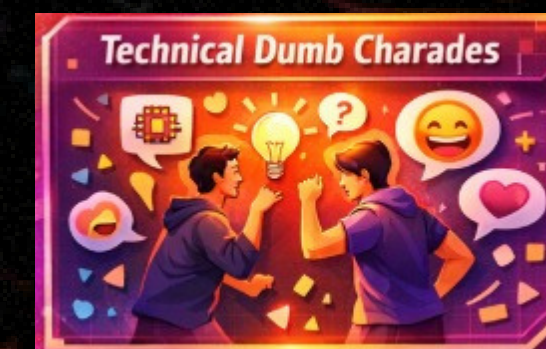
⌚ Last Date of Registration : 05.02.2026

🔒 Only Registered Students Can Participate in the Symposium  
(No Spot Registration)

### TECHNICAL EVENTS



### NON-TECHNICAL EVENTS







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**DEPARTMENT OF ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY)  
ORGANIZES**

# NEXUS CHIPCRAFT'26

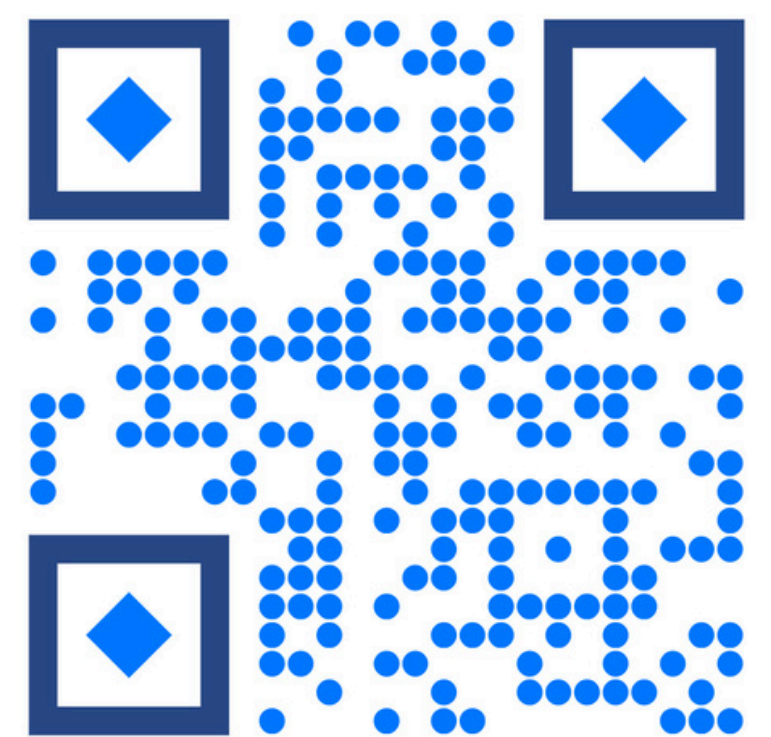
ON  
9<sup>th</sup> February 2026

## CONNECTIONS

"Think- Link-Win"

### GAME GUIDELINES

- Team Size: 2-3 members
- Rounds: Multiple rounds (3-5)
- Clues: 4-5 clues per question
- Time Limit: 30-60 seconds per question
- Scoring: +10 for correct, 0 / - marks for wrong
- Fair Play: No mobiles or external help
- Discipline: Quiet discussion only



Scan to Register!

### PRIZES & CERTIFICATES

- Participation Certificates for All Participants
- Cash Prizes for Winning Teams

*Registration Closes On  
5<sup>TH</sup> FEBRUARY*

**EVENT ORGANIZERS**

**Nandhika**

**Kamatchi**

**Contact No: 63697 41210**

**81241 79440**



nexuschipcraft26@gmail.com



Website





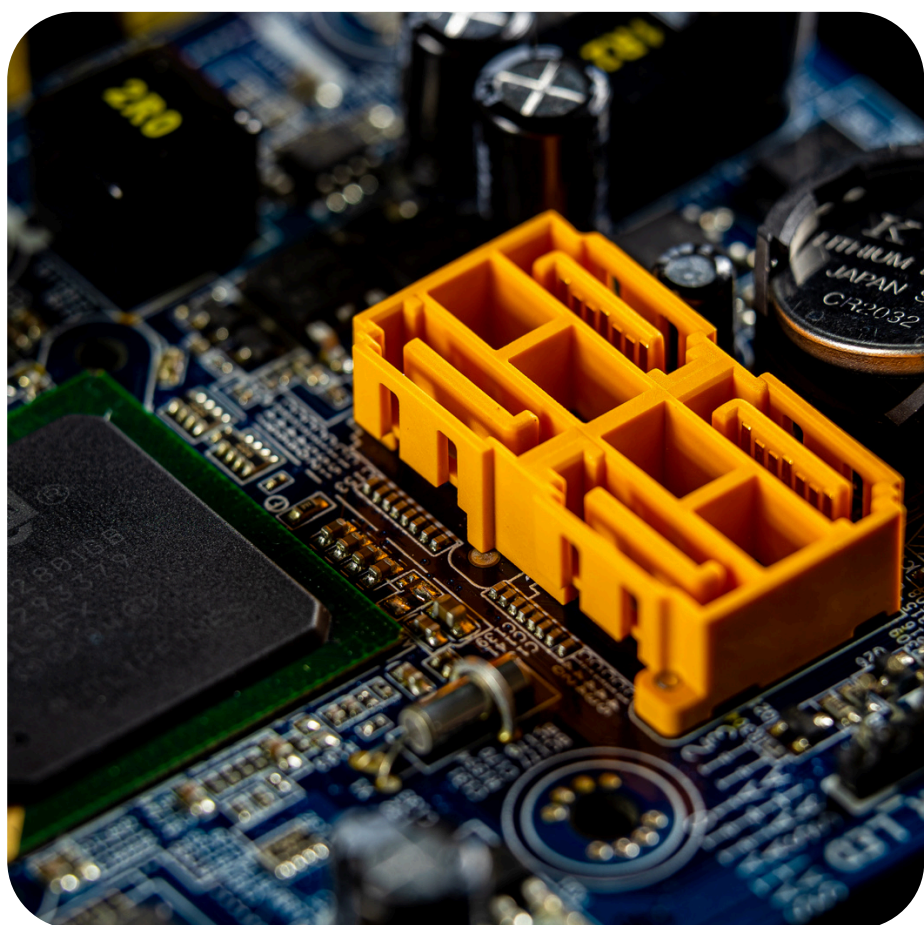
R.M.K. College of Engineering and Technology  
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# Department of Electronics Engineering (VLSI Design and Technology)

presents

## MINI PROJECT PRESENTATION



**Build.**

**Present.**

**Win.**

### Contest Info



**Event Date: 9<sup>th</sup> February 2026**

**Team Size: 1-4 Members**

**Presentation Time: 6 Minutes**

**Last Date to Register: 5<sup>th</sup> February  
2026**

### Evaluation Criteria

**Judging based on:** Innovation,  
Technical knowledge, Implementation,  
Clarity, Domain relevance

### Rules and Regulation!

- Bring **Student ID + Registration Proof**
- Working model mandatory
- N rounds (based on participants)
- Explain concept + working clearly
- VLSI & Electronics projects get **higher priority**

**Scan to  
Register Now!**

**Cash Prize Will be  
awarded for the  
Winner!!**



**Contact Us!**

Student Coordinator

Rohan S

**9345349407**



**nexuschipcraft26@gmail.com**



**Instagram**





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## Department of Electronics and Engineering (VLSI Design and Technology)



# CIRCUIT DEBUGGING



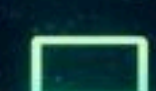




Turning Faults into Function

*Experience the Thrill of Debugging*

on

**09<sup>th</sup> February 2026**

### EVENT GUIDELINES

-  Team Size: 1-3 Engineers
-  Mandatory: Valid Student ID & Registration Proof
-  Hardware: Teams must bring your laptop.
-  Software: Verilog simulation tools pre-installed.
-  Task: Fix the provided faulty Verilog circuit
-  Structure: Event consists of 'N' elimination rounds
-  Violation: Immediate Disqualification



**Secure Your Slot**

 **Last Call: 5th Feb**

Student Coordinator :

**R.RITHIKA**

**9444601112**







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DEPARTMENT OF ELECTRONICS ENGINEERING  
(VLSI DESIGN & TECHNOLOGY)

*PRESENTS*

# NEXUS CHIPCRAFT'26

## DUMB CHARADES

**LAST DATE TO REGISTER: 05/02/2026**

**EVENT DATE: 09/02/2026**

Show your talent here and  
step into the spotlight.

### SEGMENTS



Scan to  
register now!

1

- 1 person acts
  - No speaking, no writing, no lip movement
  - 1 crt answer 10pts
- ToT time: 60sec

**BASIC ELECTRONICS**

2

- New person must act.
  - No alphabets, nos or symbols allowed by hand.
  - 1 correct answer 15pts.
- ToT time: 45sec's

**ECE CORE CONCEPT**

3

- Third member must act.
  - No pointing at objs or people.
- ToT time: 30sec's

**ADVANCED TECH**

Cash Prize Will be awarded for the **Winner!!**

**Note:** Bring Student ID + Registration Proof

Contact us

STUDENT COORDINATOR

**EATHESWAR**

**9363813334**



nexuschipcraft26@gmail.com

**FOR MORE INFO**



LinkedIn



Instagram





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GUMMIDIPOONDI TALUK, THIRUVALLUR DISTRICT, TAMIL NADU-601206



DEPARTMENT OF ELECTRONICS ENGINEERING

VLSI DESIGN AND TECHNOLOGY

PRESENTS



NEXUS CHIPCRAFT'26

TECHNICAL QUIZ

CASH PRIZE  
AWAITS YOU

**LAST DATE TO REGISTER:**  
**05/02/2026**

On: **09/02/2026**

**REGISTER NOW**



**EVENT ORGANIZERS:**

Shasank Reddy .S  
Contact Number: 9966557677

Poorneshwar  
Contact Number: 9444527061

**RULES & REGULATIONS:**

Valid Student ID & registration proof required

2–3 members per team

3–5 quiz rounds

30–60 sec per question

First buzzer gets to answer

No AI, mobiles, internet, or references

Negative marking applies

Rule violations → Disqualification



DEPARTMENT OF ELECTRONICS  
ENGINEERING VLSI DESIGN AND  
TECHNOLOGY



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